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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10044364	FILING DATE 01/11/2002	CLASS 711	SUBCLASS 167	GAU 2186	EXAMINER Mark T. Ho
<b>**APPLICANTS:</b> Wilson Kenneth; Aglietti Robert; 2188					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A CIP OF 09/896,043 06/28/2001 <i>TH</i>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> <i>No</i>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO 10012380-1	
Verified and Acknowledged Examiners's initials <i>TH</i>					
TITLE : Managing latencies in accessing memory of computer systems					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		Assistant Examiner	<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		Primary Examiner	<b>DRAWING</b>	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	Application Examiner	
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